Application Data Sheet

Application Information

Application Type::	Regular
Subject Matter::	Utility
Suggested Group Art Unit::	N/A
CD-ROM or CD-R?::	None
Sequence submission?::	None
Computer Readable Form (CRF)?::	No
Title::	CIRCUIT ARRANGEMENT FOR GENERATING NON-OVERLAPPING CLOCK PHASES
Attorney Docket Number::	20046/0200828-US0
Request for Early Publication?::	No
Request for Non-Publication?::	No
Suggested Drawing Figure::	Fig. 6
Total Drawing Sheets::	5
Small Entity?::	No
Petition included?::	No
Secrecy Order in Parent Appl.?::	No

Applicant Information

Applicant Authority Type::	Inventor
Primary Citizenship Country::	Austria
Status::	Full Capacity
Given Name::	Gebhard
Family Name::	Melcher
City of Residence::	Weinitzen
Country of Residence::	Austria
Street of mailing address::	Weinbergweg 6
City of mailing address::	Weinitzen
Country of mailing address::	Austria

Initial 02/27/04

Postal or Zip Code of mailing address:: A-8045

Correspondence Information

Correspondence Customer Number:: 07278

Representative Information

Representative Customer Number:: 07278

Domestic Priority Information

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Application	Continuation of	PCT/DE02/02926	08/08/02

Foreign Priority Information

Country::	Application number::	FilingDate::	Priority Claimed::
Germany	101 42 657.7	08/31/01	Yes

Assignee Information

Assignee name:: Infineon Technologies AG

Street of mailing address:: St.-Martin-Strasse 53

City of mailing address:: Munich

Country of mailing address:: Germany

Postal or Zip Code of mailing address:: 81669

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